

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of: Zia Hossain et al.

Serial No.: 10/729,292

Filing Date: December 8, 2003

Group Art Unit: 2815

Examiner: Joseph H. Nguyen

Title: **LATERAL FET STRUCTURE WITH IMPROVED  
BLOCKING VOLTAGE AND ON RESISTANCE  
PERFORMANCE AND METHOD**



I HEREBY CERTIFY THAT THIS CORRESPONDENCE IS BEING DEPOSITED  
WITH THE UNITED STATES POSTAL SERVICE AS FIRST CLASS MAIL IN  
AN ENVELOPE ADDRESSED TO: COMMISSIONER FOR PATENTS, P.O. Box  
1450 Alexandria, VA 22313-1450, ON: July 1, 2005

Date of Deposit

Semiconductor Components Industries, LLC.

Name of Assignee

Lydia Mchamara

SIGNATURE

7/1/05

DATE

AMENDMENT

HONORABLE COMMISSIONER FOR PATENTS,  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

In response to the Advisory Action mailed April 5, 2005,  
please reconsider the above-identified patent application in view  
of the amendments and remarks presented hereinafter.

*10 pages faxed  
orig. Dwg. via mail  
Lmc*